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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	<i>Complete if Known</i>	
	Application Number	Unknown
	Filing Date	Even Date Herewith
	First Named Inventor	Narendra, Siva
	Group Art Unit	Unknown
	Examiner Name	Unknown
Sheet 1 of 1	Attorney Docket No: 884.575US2	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS						
Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				T <sup>2</sup>
DM		LEE, CHEOL-HEE , et al., "A Temperature and Supply Insensitive CMOS Current", <u>EEE ICVC</u> , Available at <a href="http://asic.postech.ac.kr/2.Research/2.Publications/iconfer/08.pdf">http://asic.postech.ac.kr/2.Research/2.Publications/iconfer/08.pdf</a> , (October 1997), 498-500				
DM		LEE, SEUNG-HOON , "A Temperature and Supply-Voltage Insensitive CMOS Current Reference", <u>IEICE Trans. Electron</u> , Vol. E82-C, (Aug. 1999), pp. 1562-1566				
DM		LEE, C.-H. , et al., "All-CMOS Temperature Independent Current Reference", <u>Electronics Letters</u> , Vol. 32, No. 14, (July 4, 1996), pp. 1280-1281				
DM		NARENDRA, S , et al., "Sub-1 V Process-Compensated MOS Current Generation Without Voltage Reference", <u>IEEE Symposium on VLSI Circuits Digest of Technology Papers</u> , (2001), pp. 143-144				

EXAMINER

DATE CONSIDERED

1/20/05

Substitute Disclosure Statement Form (PTO-1449)

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